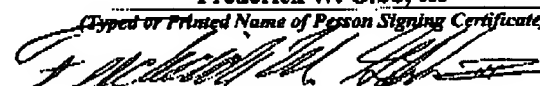


CERTIFICATE OF TRANSMISSION BY FACSIMILE (37 CFR 1.8) Applicant(s): Divakaruni et al.			Docket No. BUR920000016US1
Serial No. 09/718,850	Filing Date November 22, 2000	Examiner Samuel Gebremariam	Group Art Unit 2811
Invention: LOGIC STRUCTURE PROCESS AND APPLICATION FOR VERTICLE BIPOLAR TRANSISTOR			
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<p>I hereby certify that this <u>Notice of Appeal</u> (Identify type of correspondence)</p> <p>is being facsimile transmitted to the United States Patent and Trademark Office (Fax. No. <u>703-872-9306</u>)</p> <p>on <u>March 30, 2004</u> (Date)</p> <div style="text-align: center;"><p>Frederick W. Gibb, III (Typed or Printed Name of Person Signing Certificate)</p> (Signature)</div>			
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P18/REV01

**NOTICE OF APPEAL FROM THE PRIMARY EXAMINER TO
THE BOARD OF PATENT APPEALS AND INTERFERENCES (Large Entity)**Docket No.
BUR920000016US1

In Re Application Of: Divakaruni et al.

Serial No.
09/718,850Filing Date
November 22, 2000Examiner
Samuel GebremariamGroup Art Unit
2811

Invention: LOGIC STRUCTURE PROCESS AND APPLICATION FOR VERTICLE BIPOLAR TRANSISTOR

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OFFICIALTO THE COMMISSIONER FOR PATENTS:

Applicant(s) hereby appeal(s) to the Board of Patent Appeals and Interferences from the decision of the Primary Examiner dated December 30, 2003 finally rejecting Claim(s) 1-5, 7-17 and 23-25

The fee for this Notice of Appeal is: \$330.00

- ☐ A check in the amount of the fee is enclosed.
- ☒ The Director has already been authorized to charge fees in this application to a Deposit Account.
- ☒ The Director is hereby authorized to charge any fees which may be required, or credit any overpayment to Deposit Account No. 09-0456


Signature

Dated: 3/24/04

Frederick W. Gibb, III
Registration No. 37,629
McGinn & Gibb, PLLC
2568-A Riva Road, Suite 304
Annapolis, MD 21401
(301) 261-8071
Customer No. 29154

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